

A perspective view of a printed circuit board assembly 10. The assembly includes a substrate 14 with a top surface covered by a pattern of conductive traces 16. Various electronic components 18 are mounted on the traces. Labels include 10 for the entire assembly, 14 for the substrate, 16 for the traces, and 18 for the components.

Fig. 1

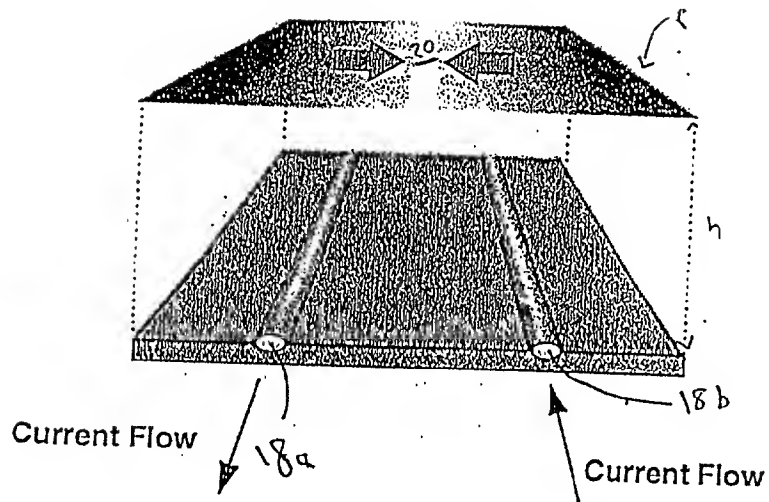


Fig. 2

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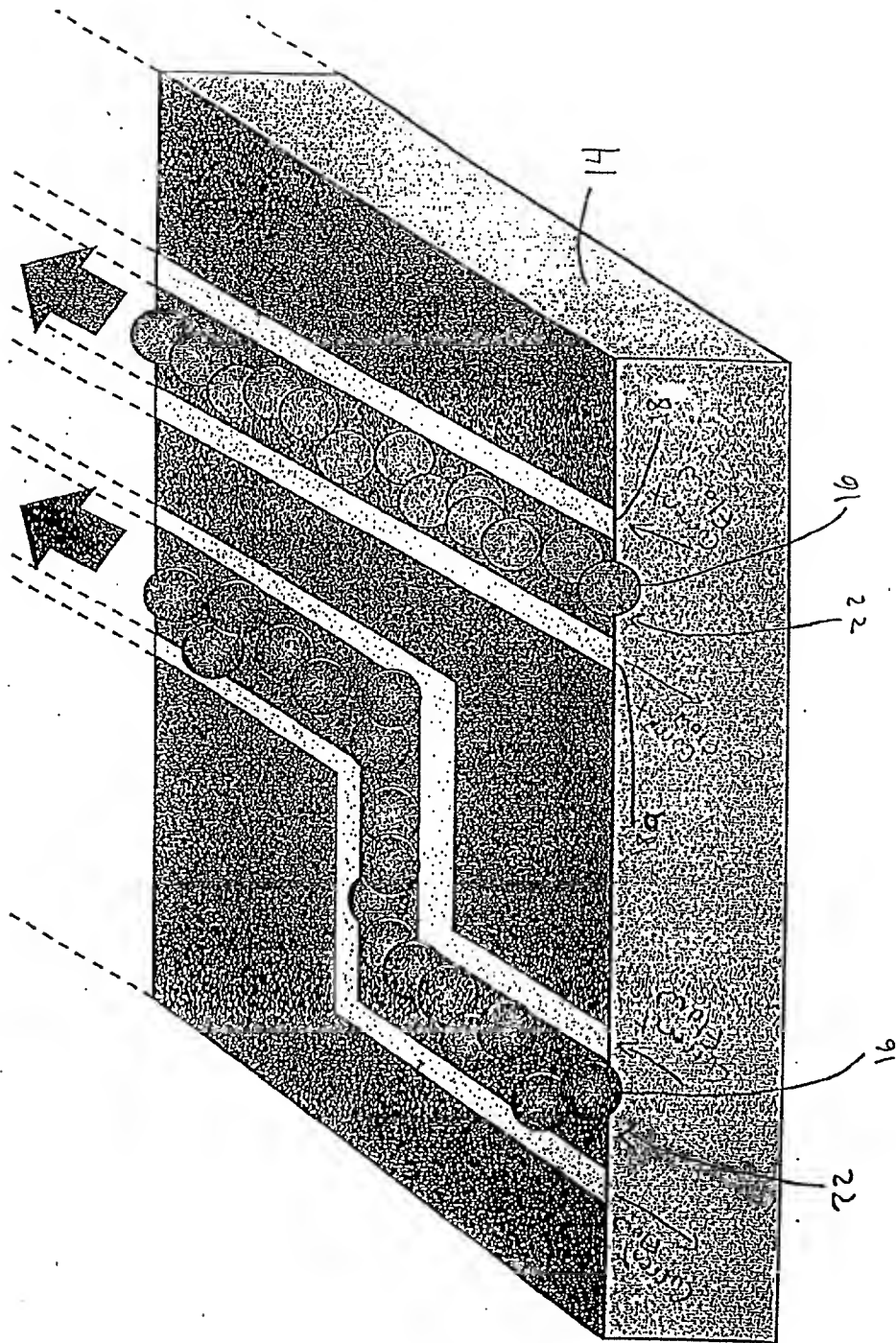
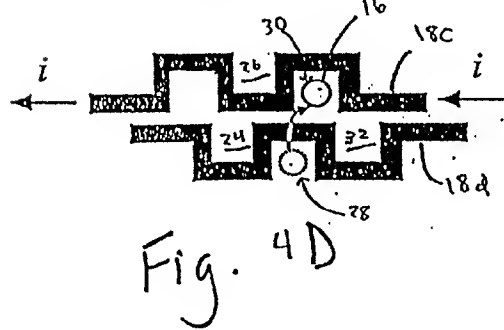
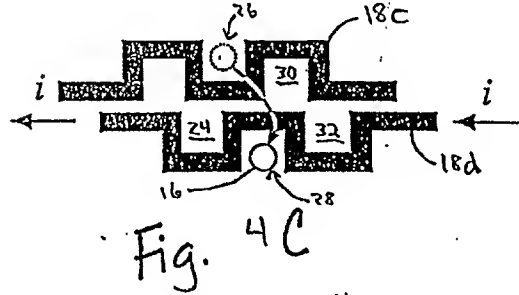
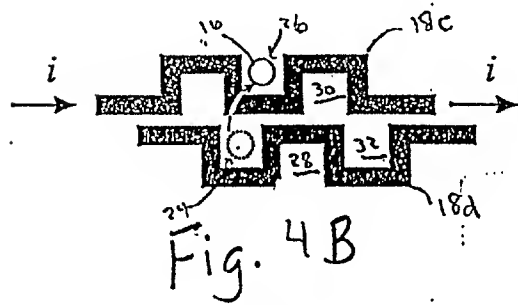
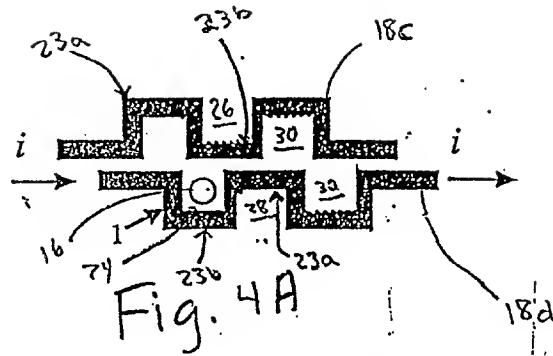


Fig. 3



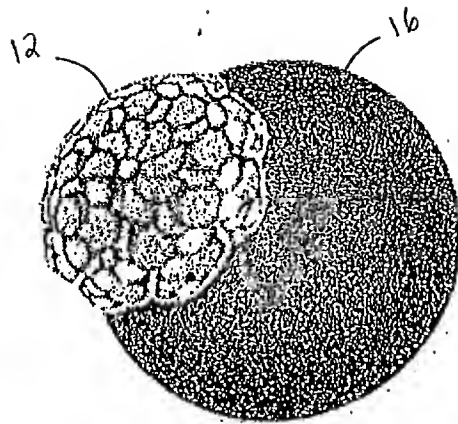


Fig. 5

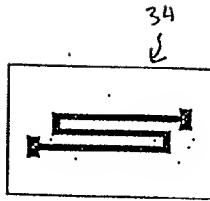


Fig. 6A

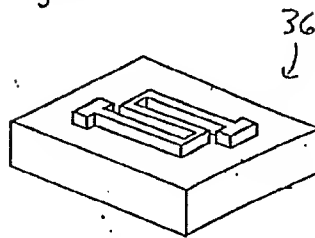


Fig. 6B

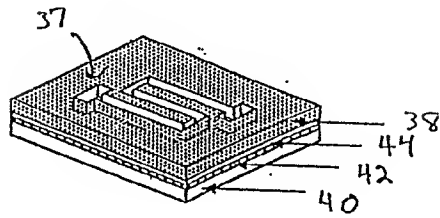


Fig. 6C

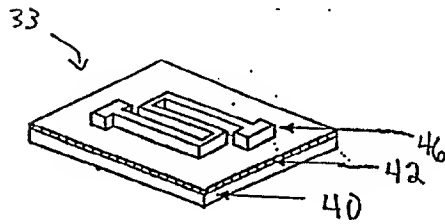


Fig. 6D

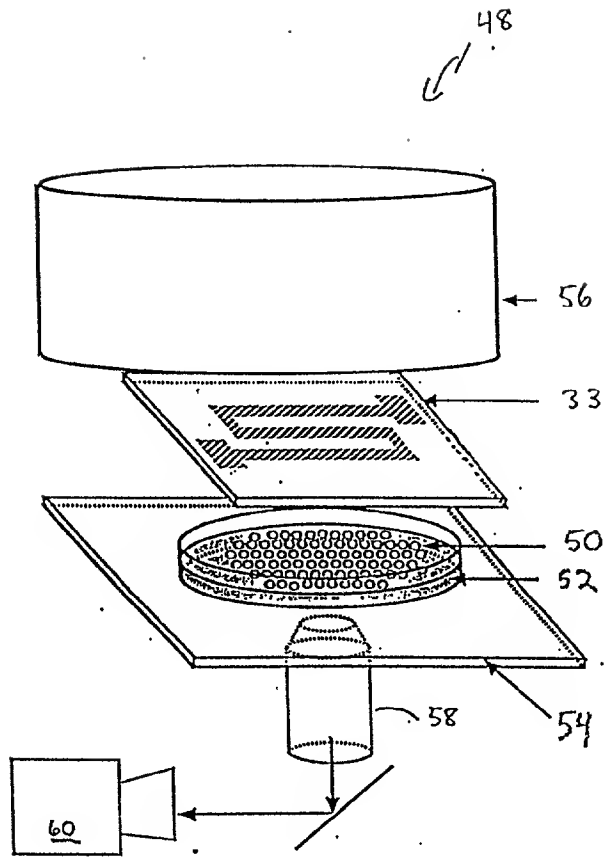


Fig. 7

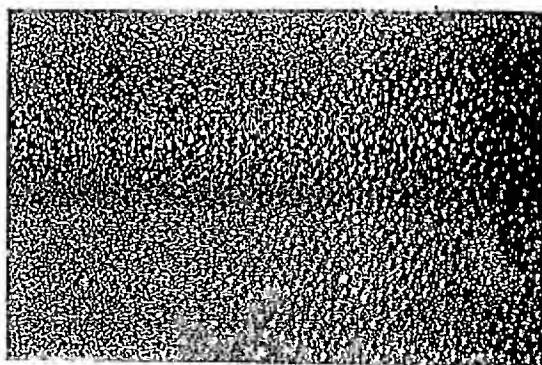


Fig. 8A

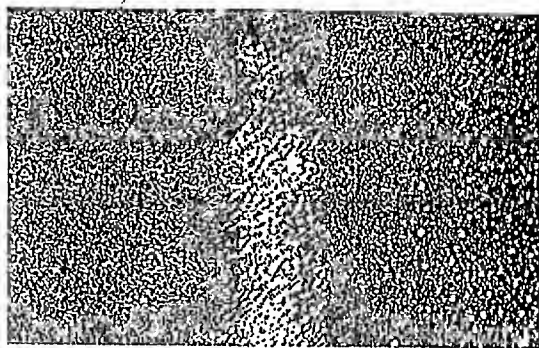


Fig. 8B



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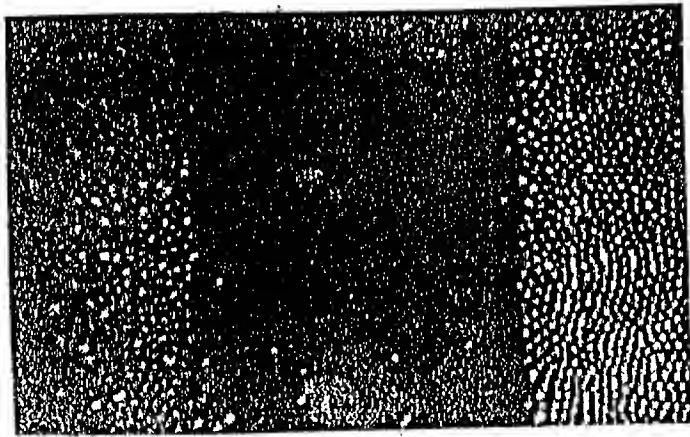
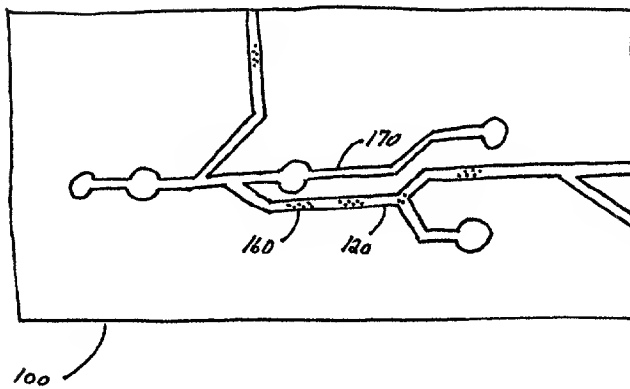


Fig. 8C

Fig. 7



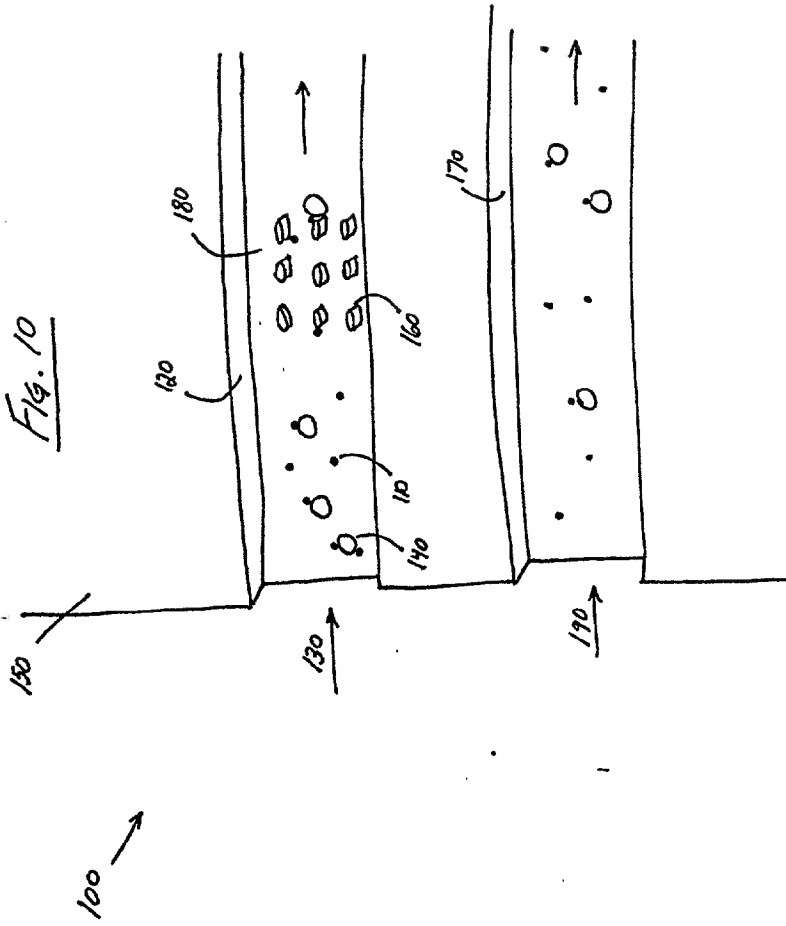


Fig. 11

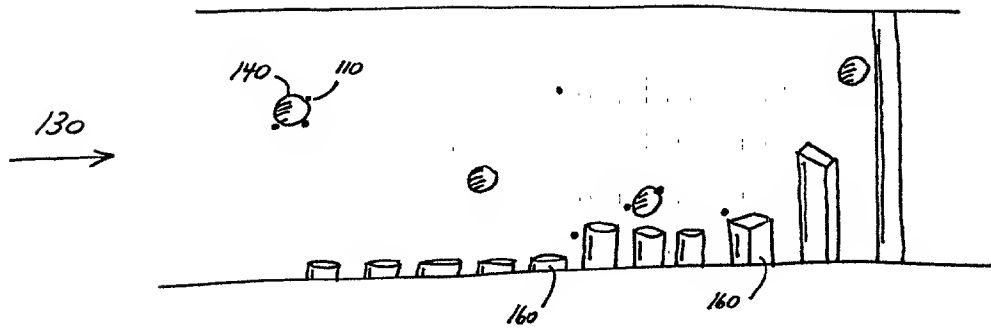
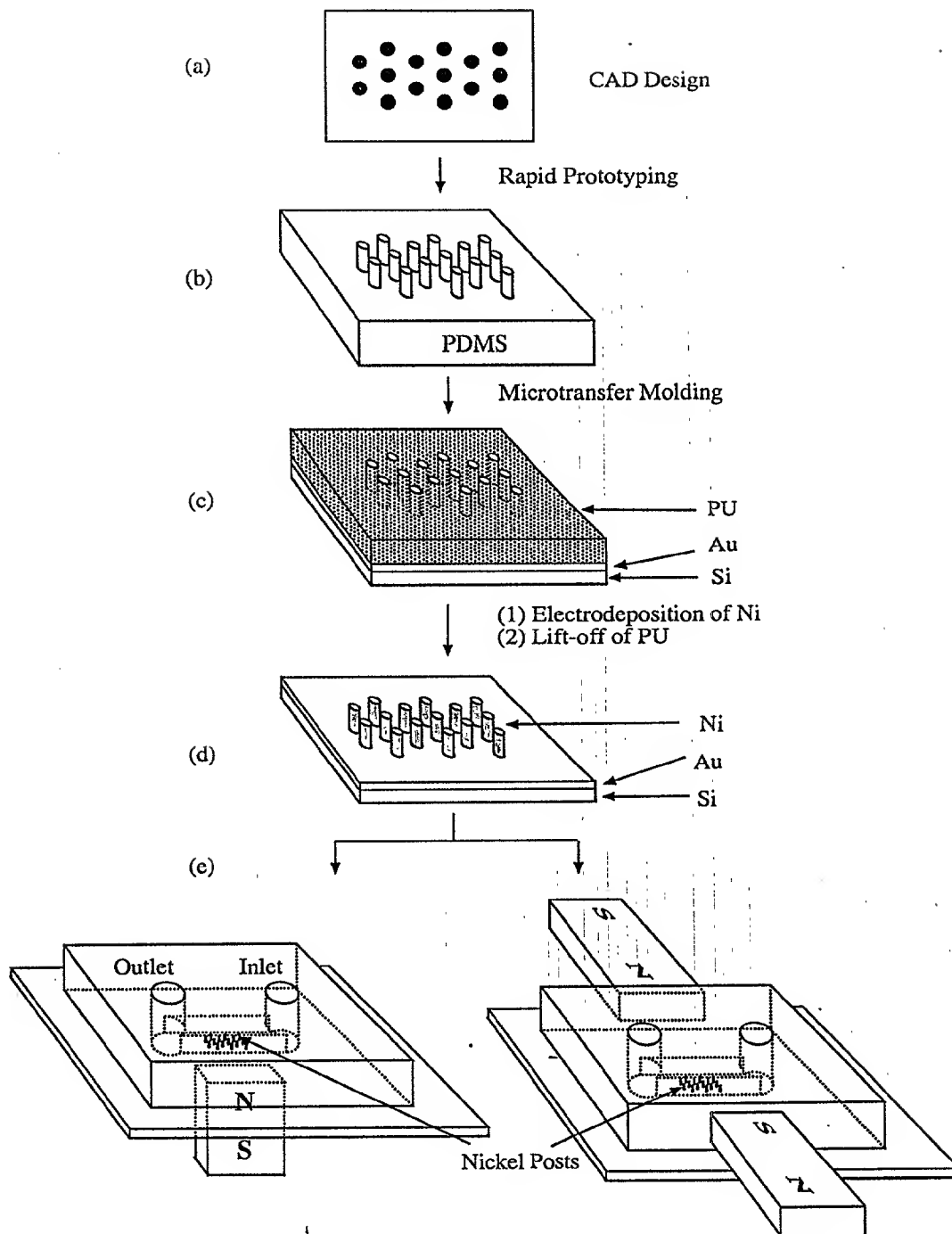


FIG. 12



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FIG. 13

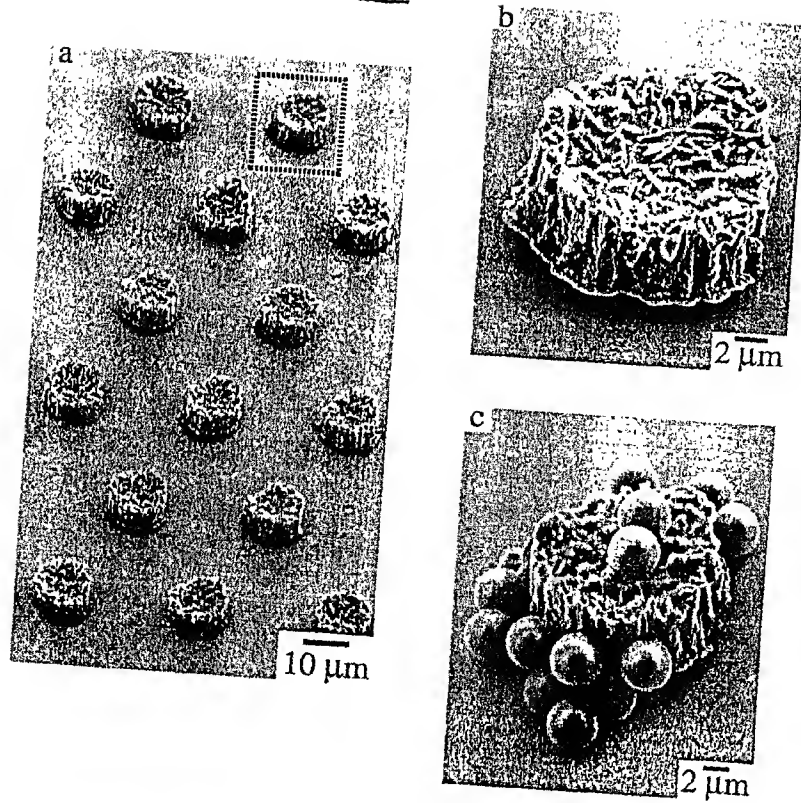


Fig. 14

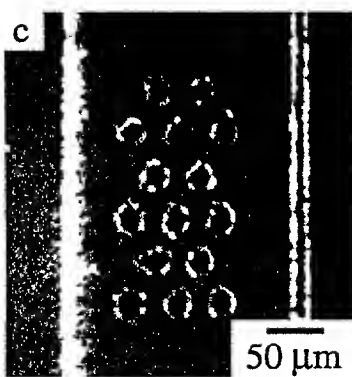
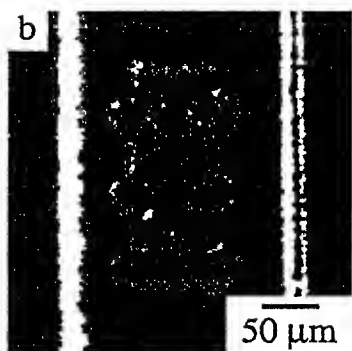
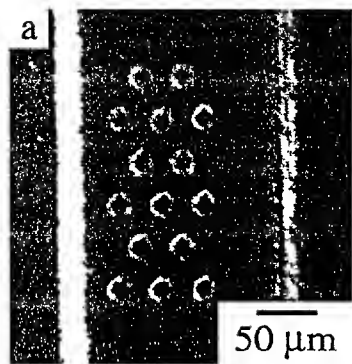


Fig. 15

